Silicon N-Channel MOS FET

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ADE-208-1244 (Z) 1st. Edition Mar. 2001

Application

Low frequency power amplifier

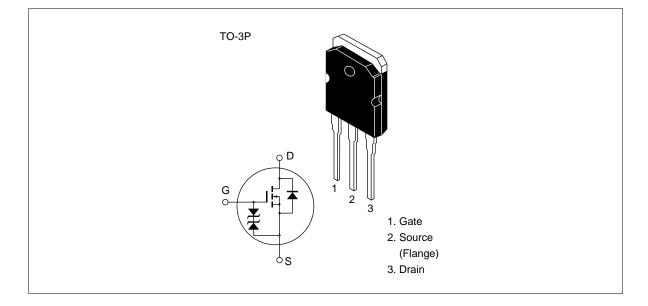
Complementary pair with 2SJ160, 2SJ161 and 2SJ162

Features

- Good frequency characteristic
- High speed switching
- Wide area of safe operation
- Enhancement-mode
- Good complementary characteristics
- Equipped with gate protection diodes
- Suitable for audio power amplifier



Outline



Absolute Maximum Ratings (Ta = 25°C)

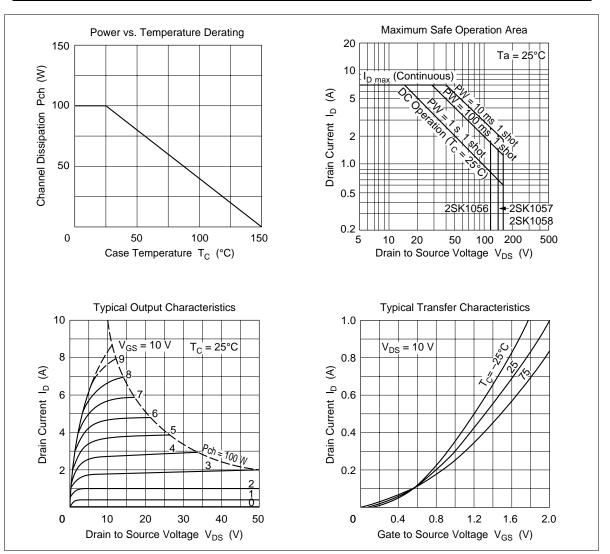
V _{DSX}		
V DSX	120	V
	140	
	160	
V _{GSS}	±15	V
I _D	7	А
I _{DR}	7	А
Pch*1	100	W
Tch	150	°C
Tstg	-55 to +150	°C
	I _D I _{DR} Pch*1 Tch	I60 V _{GSS} ±15 I _D 7 I _{DR} 7 Pch* ¹ 100 Tch 150

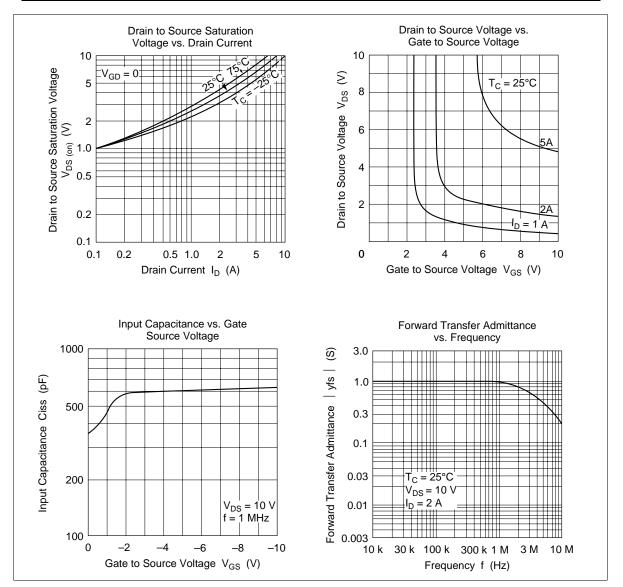
Note: 1. Value at $T_c = 25^{\circ}C$

Electrical Characteristics (Ta = 25°C)

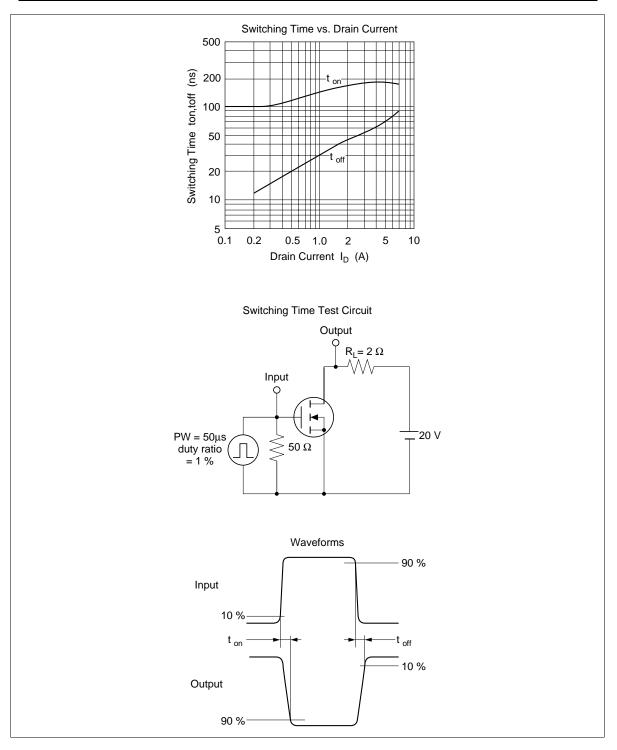
Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source	2SK1056	$V_{(BR)DSX}$	120		_	V	$I_{\rm D} = 10$ mA, $V_{\rm GS} = -10$ V
breakdown voltage	2SK1057	_	140				
	2SK1058	_	160				
Gate to source break voltage	kdown	$V_{(BR)GSS}$	±15	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source cutof	f voltage	$V_{\text{GS(off)}}$	0.15	_	1.45	V	$I_{\rm D}$ = 100 mA, $V_{\rm DS}$ = 10 V
Drain to source satu voltage	ration	$V_{\text{DS(sat)}}$	_	_	12	V	$I_{\rm D} = 7$ A, $V_{\rm GD} = 0$ * ¹
Forward transfer adr	nittance	yfs	0.7	1.0	1.4	S	$I_{D} = 3 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Input capacitance		Ciss		600	_	pF	$V_{GS} = -5 V, V_{DS} = 10 V,$
Output capacitance		Coss		350	_	pF	f = 1 MHz
Reverse transfer cap	pacitance	Crss	_	10	_	pF	_
Turn-on time		t _{on}		180	—	ns	$V_{DD} = 20 \text{ V}, \text{ I}_{D} = 4 \text{ A},$
Turn-off time		t _{off}		60	_	ns	_

Note: 1. Pulse test



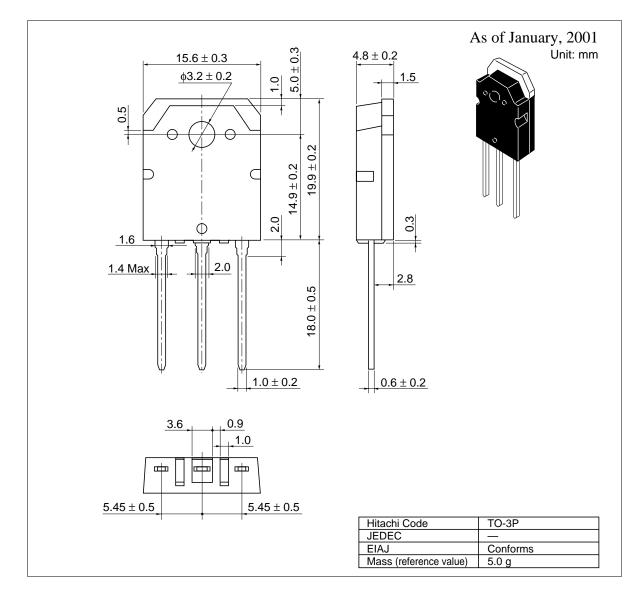


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Package Dimensions



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